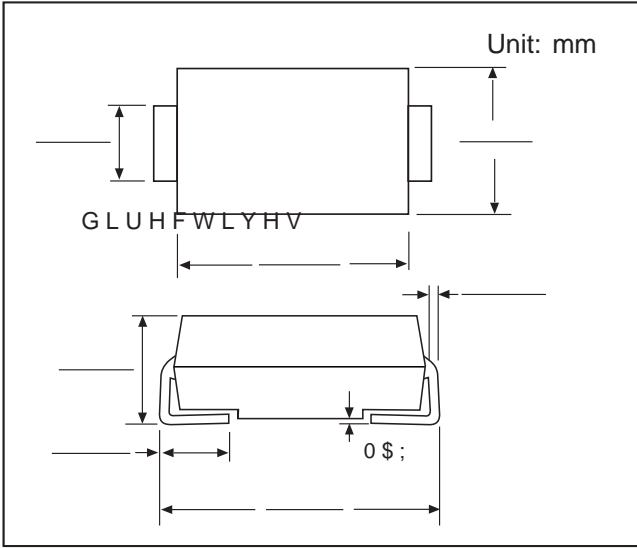


6 0 & 3 / \$ 6 7 6 & / , & 2 5 ( & 7 , ) , ( 5 6

)( \$ 7 8 5 ( 6  
 ") R U V X U I D F H P R X Q W H G D S S O L F D W L R Q V  
 "/ R Z S U R I L O H S D F N D J H  
 "\* O D V V 3 D V V L Y D W H G & K L S - X Q F W L R Q  
 "( D V \ W R S L F N D Q G S O D F H  
 "/ H D G I U H H L Q F R P S O \ Z L W K ( 8 5 R + 6

0 ( & + \$ 1 , & \$ / ' \$ 7 \$

& D V H ~~on~~ed plastic body over passivated chip  
 7 H U P L S ~~on~~ated, solderable per MIL-STD-750, Method 2026  
 3 R O D C ~~on~~band denotes cathode end



0 \$ ; , 0 8 0 5 \$ 7 , 1 \* 6 \$ 1 ' & + \$ 5 \$ & 7 ( 5 , 6 7 , & 6

# f & \$ P E L H G W S H U D X X O R H W K H U Z R L W H G

Parameter	Symbols								Units
Maximum Repetitive Peak Reverse Voltage	$V_{RRM}$	50	100	200	400	600	800	1000	V
Maximum RMS voltage	$V_{RMS}$	35	70	140	280	420	560	700	V
Maximum DC Blocking Voltage	$V_{DC}$	50	100	200	400	600	800	1000	V
Maximum Average Forward Rectified Current		5							A
Peak Forward Surge Current 8.3 ms Single Half Sine Wave Superimposed on Rated Load		120							A
Maximum Instantaneous Forward Voltage at 5 A		1.0							V
Maximum DC Reverse Current at Rated DC Blocking Voltage	$I_{RT}$	5 100							1/4 A
Typical Junction Capacitance		50							pF
		35 13							
Operating and Storage Temperature Range		-55 ~ +150							°

1 Measured at 1 MHz and reverse voltage of 4 V D.C.  
 2

5 7, 1 \* 6 1' & + 5 7 (5, 6 7, & & 8 5 9 (6

